

FPGA DIGITAL LOGIC DESIGN



● ● ● FPGA Digital Logic Design



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FPGAs Digital Logic Design uses the best FPGA device configured as a module, which enables selectable use of Intel (Altera) or Xilinx FPGAs. You can control the environment of Multi Media via TFT LCD and CMOS Image Sensor in the equipment. This product also can be controlled on Android Smartphone by configuring Bluetooth for wireless communication.

▶▶▶ Product Features

- Uses FPGA (Intel, formerly Altera - Cyclone 10 Series, Xilinx-Spartan 7 Series) Devices
- Modularized to enable the replacement of Altera and Xilinx devices, taking into account the flexibility and scalability of FPGA devices
- Provides systematic training contents for Digital Logic Design
- Provides 16 types of clock between 0Hz - 50MHz configured in Clock Control Block for application circuit
- 5"-TFT LCD, CMOS Image Sensor, Audio Block and SD Socket for multi-media control
- Full color LED, 7-segment display device, step motor, piezo enable various application design
- Able to design sensor application circuit and analog signal control design through ADC / DAC
- Configure the expansion port to control other devices outside the board
- Organize textbook contents for users to design application examples using devices configured in equipment as well as basic digital logic design using FPGA

▶▶▶ Hardware Specifications

| Category | Sub-category | Specifications |
|----------------|---------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| FPGA | Intel Module (default) | <ul style="list-style-type: none"> • Cyclone 10 LP Device : 10CL080YF484 • Logic Cells : 81,264 • Embedded Memory : 2,745kbit • Embedded 18x18 Multiplier : 244 • Configuration ROM • Power Block : IN (+5V, +3.3V) Generation (+1.2V, +2.5V) • JTAG Port, Reconfiguration Switch |
| | Xilinx Module (option) | <ul style="list-style-type: none"> • Spartan 7 Device : XC7S75A484 • Logic Cells : 76,800 • Embedded Memory : 3,240kbit • DSP Slices : 140 • Configuration ROM • Power Block : IN (+5V, +3.3V) Generation (+1.2V, +1.8V, +2.5V) • JTAG Port, Reconfiguration Switch |
| Base | Clock Block | <ul style="list-style-type: none"> • Provides 16 levels of clock from 0 Hz to 50 MHz [0Hz, 1Hz, 10Hz, 50Hz, 100Hz, 500Hz, 1kHz, 5kHz, 10kHz, 50kHz, 100kHz, 500kHz, 1MHz, 5MHz, 25MHz, 50MHz] • 3 digit 7-Segment and LED to check the set clock |
| | TFT LCD | <ul style="list-style-type: none"> • 5inch, 800 x RGB x 480 pixel, 24bit |
| | CMOS Image Sensor | <ul style="list-style-type: none"> • 0.3M pixel, 1/6inch, 640x480 VGA • Output Support for Raw RGB, RGB, YUV, YCbCr |
| | Audio | <ul style="list-style-type: none"> • Stereo Audio Codec, 8 ~ 96kHz, Integrated Headphone Amplifier |
| | SD Socket | <ul style="list-style-type: none"> • T Flash Memory Socket |
| | Memory | <ul style="list-style-type: none"> • 4Mbit SRAM, 256Mb SDRAM, 16kB 2Wire Serial EEPROM |
| | Display | <ul style="list-style-type: none"> • 16x2 Text LCD, 4 digit 7-Segment 2EA, LED 8EA (Diffusion Type RED), Full Color LED 4EA |
| | Actuator | <ul style="list-style-type: none"> • Step Motor 1EA with magnetic sensor (status LED 4EA) |
| | Data Conversion | <ul style="list-style-type: none"> • Parallel ADC : 1 Channel 8bit 32M Sampling Speed • Parallel DAC : 1 Channel 8bit 100M Sampling Speed |
| | Input | <ul style="list-style-type: none"> • Push Button Switch 8EA, DIP Switch 8EA, 3x4 Keypads |
| | ETC | <ul style="list-style-type: none"> • Piezo |
| | Communication | <ul style="list-style-type: none"> • Bluetooth • 2 Channel UART (USB to Serial 1 Port , D-Sub 9 Pin 1 Port) |
| Expansion Port | <ul style="list-style-type: none"> • 2x25 Box Header (44pin I/O) | |
| Size | 500 x 290 x 160(mm), Case | |

▶▶▶ Software Specifications

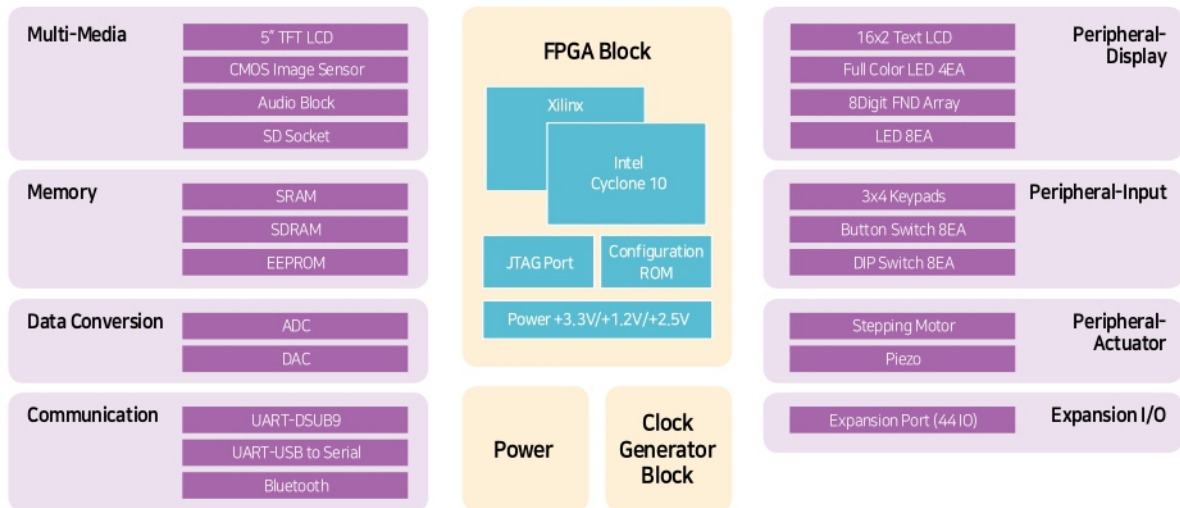
| Category | Specifications |
|---------------|-------------------------------|
| Intel Module | Quartus II Prime Lite Edition |
| Xilinx Module | Vivado HL WebPACK Edition |

▶▶▶ Training Contents

[Digital Logic Circuit Design and Control Using FPGA]

- ▶ Installing Design Tool and Getting Started
- ▶ Experiments for Control Logic and Application Design
 - LED / Button Switch / DIP Switch / 7-Segment / Keypads / Piezo / Step Motor
 - Full Color LED / Text LCD / DAC / ADC / RS232 / USB to Serial / Audio
 - SRAM / SDRAM / EEPROM / TFT LCD / CMOS Image Sensor / SD Socket / Bluetooth
- ▶ Experiments for Multi-Media Application

▶▶▶ Block Diagram



▶▶▶ Layout



- | | |
|------------------|-------------------------------|
| 1 ADC Block | 13 Clock Generator Block |
| 2 Full Color LED | 14 Piezo |
| 3 DAC Block | 15 Stepping Motor with Sensor |
| 4 Bluetooth | 16 FPGA Module |
| 5 Text LCD | 17 CMOS Image Sensor |
| 6 USB to Serial | 18 SRAM |
| 7 FND Array | 19 SDRAM |
| 8 RS232C | 20 EEPROM |
| 9 LED | 21 Expansion Port |
| 10 Button Switch | 22 SD Socket |
| 11 Keypads | 23 5' TFT LCD |
| 12 DIP Switch | 24 Audio Block |